

LV-PECL 3.3V Oscillator



F4600U SERIES

RoHS Compliant / Pb Free / REACH Compliant

Rev. 4/24/2015

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FEATURES

- 3.3V Operation
- -40 to +85°C Operating Temperature
- LV-PECL Output
- 7x5mm Ceramic Package
- Low Phase Jitter (0.1pS RMS Max)

• PART NUMBER SELECTION

Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
535U-Frequency-xxxxx	F4600U	±100PPM	-10 ~ +70	70.000 ~ 170.000
536U-Frequency-xxxxx	F4600RU	±100PPM	-40 ~ +85	70.000 ~ 170.000
537U-Frequency-xxxxx	F4605U	±50PPM	-10 ~ +70	70.000 ~ 170.000
538U-Frequency-xxxxx	F4605RU	±50PPM	-40 ~ +85	70.000 ~ 170.000
539U-Frequency-xxxxx	F4606U	±25PPM	-10 ~ +70	70.000 ~ 170.000
540U-Frequency-xxxxx	F4606RU	±25PPM	-40 ~ +85	70.000 ~ 170.000
541U-Frequency-xxxxx	F4608U	±20PPM	-10 ~ +70	70.000 ~ 170.000

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	70.000 ~ 170.000 MHz
Storage Temperature Range (TSTG)	-55°C ~ +125°C
Supply Voltage (VDD)	3.3V ± 5%
Input Current (IDD)	75mA
Standby Current	15uA
Output Symmetry (50% Vp-p)	45% ~ 55%
Rise Time (Tr) (20% ~ 80% Vp-p)	1nS
Fall Time (Tf) (80% ~ 20% Vp-p)	1nS
Output Voltage (VOL) (VOH)	1.68V 2.275V Min
Output Load	50 Ohms into VDD -2.0 VDC
Start-up Time (Ts)	3mS
Phase Jitter (12kHz to 20MHz)	0.1 pS RMS
Maximum Soldering Temp / Time	260°C / 10 Seconds
Termination Finish	Au over Ni

¹ Inclusive of 25°C tolerance after reflow, operating temperature range, supply voltage change, and aging.

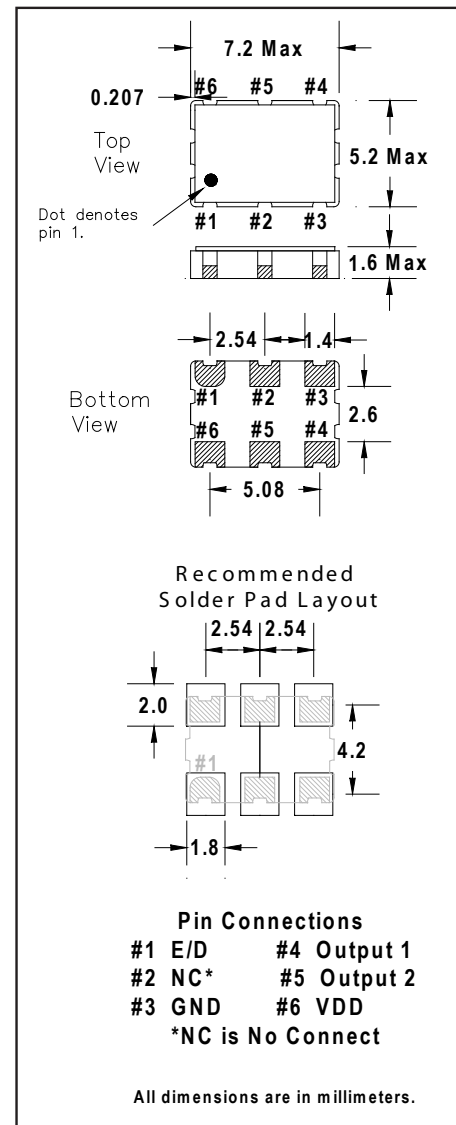
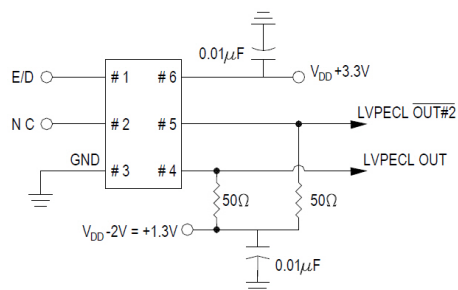
² An internal pullup resistor from pin 1 to pin 6 allows active output if pin 1 is left open.

Notes:

*A 0.01μF capacitor should be placed between VDD (Pin 6) and GND (Pin 3) to minimize power supply line noise.

*Dimensional drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary. All specifications subject to change without notice.

RECOMMENDED CIRCUIT



• ENABLE / DISABLE FUNCTION

(Pin 1)	OUTPUT (Pin 4)	OUTPUT (Pin 5)
OPEN ²	ACTIVE	ACTIVE
'1' Level VIH ≥ 70% VDD	ACTIVE	ACTIVE
'0' Level VIL ≤ 30% VDD	High Z	High Z

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