

HCMOS 1.6x1.2mm 3.3V SMD Oscillator



Model: F630 SERIES

RoHS/REACH Compliant / Pb Free

Rev. 3/16/2015

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Need a Sample®

FEATURES

- 3.3V Operation
- Standby Function
- Tape and Reel (3,000 pcs. STD)

OPTIONS

- 1.8V (F610) Version Available
- 2.5V (F640) Version Available

• PART NUMBER SELECTION

Part Number	Model Number	Frequency Stability ¹	Operating Temperature (°C)	Frequency Range (MHz)
873-Frequency-xxxxx	F630	±100PPM	-20 ~ +70	1.000 ~ 80.000
873A-Frequency-xxxxx	F630R	±100PPM	-40 ~ +85	1.000 ~ 80.000
873B-Frequency-xxxxx	F635	±50PPM	-20 ~ +70	1.000 ~ 80.000
873C-Frequency-xxxxx	F635R	±50PPM	-40 ~ +85	1.000 ~ 80.000
873D-Frequency-xxxxx	F633	±30PPM	-20 ~ +70	1.000 ~ 80.000
873E-Frequency-xxxxx	F633R	±30PPM	-40 ~ +85	1.000 ~ 80.000
873F-Frequency-xxxxx	F636	±25PPM	-20 ~ +70	1.000 ~ 80.000
873H-Frequency-xxxxx	F638	±20PPM	-20 ~ +70	1.000 ~ 80.000

• ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (Fo)	1.000 ~ 80.000 MHz
Storage Temperature Range (T _{STG})	-40°C ~ +85°C
Supply Voltage (V _{DD})	3.3V ± 10%
Input Current (I _{DD})	
1.000000 ~ 10.000000 MHz	3mA
10.000001 ~ 20.000000 MHz	4mA
20.000001 ~ 30.000000 MHz	5mA
30.000001 ~ 40.000000 MHz	6mA
40.000001 ~ 50.000000 MHz	7mA
50.000001 ~ 60.000000 MHz	8mA
60.000001 ~ 70.000000 MHz	9mA
70.000001 ~ 80.000000 MHz	10mA
Output Symmetry (50% V _{DD})	45% ~ 55%
Rise Time (10% ~ 90% V _{DD})	4.5nS
Fall Time (90% ~ 10% V _{DD})	4.5nS
Output Voltage (V _{OL})	10% V _{DD}
(V _{OH})	90% V _{DD} Min
Output Current (I _{OL})	4mA Min
(I _{OH})	-4mA Min
Output Load (HCMOS)	15pF
Standby Current	10µA
Start-up Time (T _s)	2mS
Output Disable Time ²	200nS
Output Enable Time ²	2mS
Maximum Soldering Temp/Time	260°C ±5°C / 5 Seconds
Moisture Sensitivity Level (MSL)	1
Termination Finish	Au over Ni

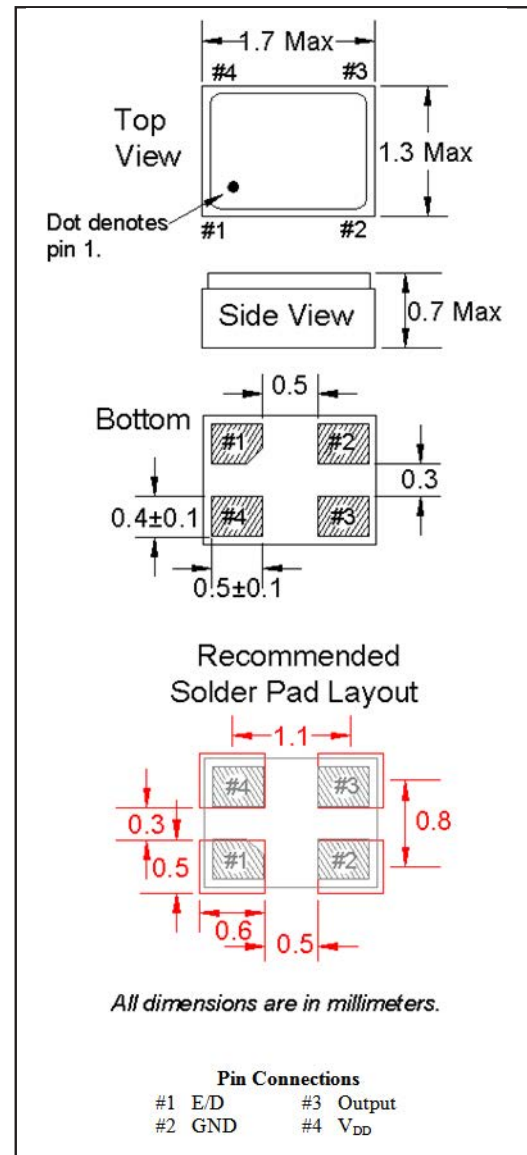
¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

² An internal pullup resistor from pin 1 to pin 4 allows active output if pin 1 is left open.

Note: A 0.01µF bypass capacitor should be placed between V_{DD} (Pin 4) and GND (Pin 2) to minimize power supply line noise.

Drawing is for reference to critical specifications defined by size measurements.

Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary. All specifications subject to change without notice.



• ENABLE / DISABLE FUNCTION

(Pin 1)	OUTPUT (Pin 3)
OPEN ²	ACTIVE
'1' Level V _{IH} ≥ 70% V _{DD}	ACTIVE
'0' Level V _{IL} ≤ 30% V _{DD}	High Z

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• TAPE SPECIFICATIONS (millimeters)							
MODEL	A	B	C	D	E	F	STD Reel QTY
F630 Series	∅1.5	4.0	4.0	3.5	8.0	0.8	3,000

• REEL SPECIFICATIONS (millimeters)							
MODEL	G	H	I	J	K	L	M
F630 Series	2.0	∅13	∅21	∅60	∅180	9.0	1.2

