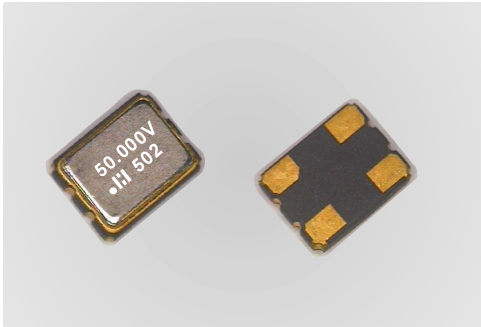


• D3SV Series 3.2*2.5 VCXO



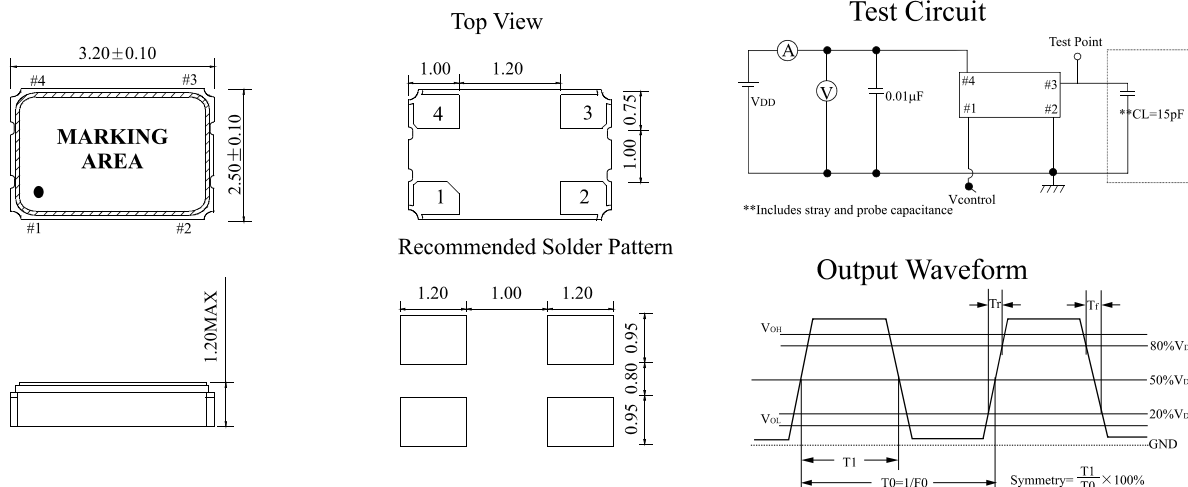
FEATURES

- Industry Standard with 3.2*2.5*1.2mm package
- TTL/HCMOS output compatible
- Tri-State Enable/Disable
- Tight tolerance performance with voltage IC control
- Designed primarily for use in phase locked loops, phase shift keying and other telecommunication applications such as ADSL, set-top box, and base stations etc.

Electrical Specifications

Parameter	Condition	D3SV	
Frequency Range*	F0	1.75~54MHz	
Frequency Calibration	At 25°C	± 15ppm	
Temperature Stability	Over T _{OPR}	± 15ppm, ± 25ppm, ± 50ppm	
Stability vs. power change	V _{DD} +/-5%	± 5ppm	
Stability vs. load change	15pF+/-10%	± 3ppm	
Pullability	Over Control Voltage Range	± 100ppm, ± 200ppm	± 100ppm, ± 200ppm
Control Voltage Range		0.5~4.5V	0~3.3V
Operating Temperature Range	T _{OPR}	0°C~+70°C (-40°C~+85°C option)	
Storage Temperature Range	T _{STG}	-55°C~+125°C	
Power Supply Voltage	V _{DD}	5.0V+/-5%	3.3V+/-5%
Aging (First Year)	25°C ± 3°C	± 5ppm	
Supply Current	I _{DD}	30mA Max	
Output Symmetry	Sym	At 1/2V _{DD} 40/60%(45/55% Option)	
Rise time	T _r	20%V _{DD} ~80%V _{DD}	8nS Max
Fall Time	T _f	80%V _{DD} ~20%V _{DD}	8nS Max
Output Voltage	V _{OH}	90% V _{DD} min	
	V _{OL}	10% V _{DD} max	
Output Load		15pF Max	
Start-up Time	T _s	10mS Max	
Packing Unit		1000pcs/reel	

Mechanical Dimensions(mm)



***note: A 0.01µF bypass capacitor should be placed between V_{DD}(Pin6) and GND(Pin3) to Minimize power supply line noise

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Specifications are subject to change without notice