SMD OSCILLATOR

Features

•Typical 7.5x5.0x1.7mm (1.4mm available) ceramic SMD package.
•Symmetry (45 to 55%) available.
•Operation voltage : 5.0V.

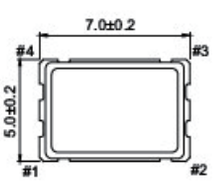
Specifications

Parameter	Min.	Max.	Unit
Supply Voltage Variation(VDD)10%	5.0	5.0	V
	4.5	5.5	V
Frequency Range	1.000000	99.99	MHz
Operating Temp. Range	-40 ~ 85		℃
Frequency Stability*	25		ppm
Supply Current			mA
1.0MHz ≤ Fo < 20MHz	-	20	
20MHz ≤ Fo < 50MHz	-	40	
50MHz ≤ Fo < 80MHz	-	50	
80MHz ≤ Fo < 125MHz	-	70	
125MHz ≤ Fo < 167MHz	-	-	
Output Level (CMOS)			
Output High (Logic "1")	90% VDD		V
Output Low (Logic "0")		10% VDD	
Transition Time : Rise/Fall Time			
1.0MHz ≤ Fo < 20MHz	-	6	nSec
20MHz ≤ Fo < 50MHz	-	5	
50MHz ≤ Fo < 80MHz	-	4	
80MHz ≤ Fo < 125MHz	-	4	
125MHz ≤ Fo < 167MHz	-	-	
Start up Time	-	10	mSec
Tri-State	Input Logic"1" or Floating : output active Input Logic"0" : output disables to high-z		
Phase Jitter		1.0	pS
Standby Current	-	10	μA
Storage Temp. Range	-55	125	℃

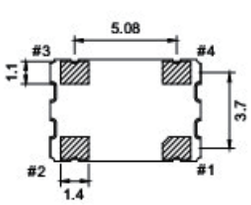
*inclusive of calibration @ 25℃, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.
+Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

DIMENSION (Unit: mm)

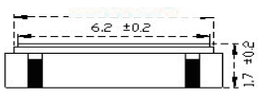
[TOP VIEW]



[BOTTOM VIEW]



[SIDE VIEW]



Pad	Function
#1	Tri-State
#2	GND
#3	Output
#4	VDD

SOLDER PAD LAYOUT(mm)