

SCOCXOH family package DIL 14 HC-MOS output 10 to 120 MHz



Package:

20.20

Pin out

Pin 1 = Voltage control

Pin 7 = GND

Pin 8 = Fout

Pin 14 = Vdd

Pin 14 = Vdd

All dimensions in mm typical

Oven control quartz crystal oscillator Fundamental mode frequency High shock and vibration resistance Wide temperature range Low aging Customer specification on request Very fast warm up Low power consumption Swiss made quality

ELECTRICAL CHARACTERISTICS 25°C

DESCRIPTION:

This DIL 14 package has been specially designed for the applications:

- Digital switching
- Telecom transmission
- Sonet / SDH / DWDM / FDM/36 / WIMAX
- Airbone equipments
- Battery operated systems
- Instrumentation
- Radio Transceiver

The OCXO are supplied on trays (50 pcs/tray).

Frequency versus temperature A: 0 to +60°C B: -20 to +70°C C: -40 to +85°C		ΔF/F	see ta (without		
Frequency long term			≤ 40MHz	>40MHz	
long term aging 10 year long term aging 1st year		ΔF/F	< ± 2.5 ≤ ± 0.3	< ± 4 ≤ ± 1	ppm
Frequency control range	a saa tahla 3	Vc	≤ 40MHz	>40MHz	
Trequency control range	e see table s	VC	≥ ± 2.5	≥ ± 4	ppm
Supply voltage		Vdd	3.3 / 5		V
Input current		ldd	see table 2		
Output signal sine wave			HC-MOS compatible		
Symmetry at Vdd/2			40 / 60		%
Rise & fall time (without load)			≤7		nS
Level "0" & "1"			<0.4V> Vcc-0.5		V
Load min / max			3/47		pF
Start-up time		t	<5		ms
Frequency stability versus load ± 10%		ΔF/F	≤ ± 30		ppb
Warm-up within ± 0.1 ppm at 25°C		Vdd	3.3	5	V
Warn-up Within ± 0.1	ppiii at 25 C	t	≤ 120	≤ 60	S
Stability versus Vdd		ΔF/F	< ± 0.1		ppm
Short term stability 0.1 to 30s 5E-11 typ at 1s		Tau	< 1		E-10
Phase noise typical			10MHz	100MHz	
Static conditions BW = 1Hz	10Hz 100Hz 1 kHz 10 kHz 100kHz		-105 -135 -150 -160 -160	-90 -120 -140 -150 -155	dBc/ Hz

TABLE 1: Vdd = 3.3V

Operating	Vdd = 3.3V ± 0.15V		
Operating Temperature range	Version standard	Version high stability	
$A = 0 \text{ to } +60^{\circ}\text{C}$	≤ ± 75 ppb	≤ ± 50 ppb	
B = -20 to +70°C	≤ ± 150 ppb	≤ ± 75 ppb	
$C = -40 \text{ to } +85^{\circ}C$	≤ ± 250 ppb	≤ ± 100 ppb	

TABLE 1: Vdd = 5V

Operating	Vdd = 5V ± 0.2V		
Operating Temperature range	Version standard	Version high stability	
$A = 0 \text{ to } +60^{\circ}\text{C}$	≤ ± 50 ppb	≤ ± 25 ppb	
B = -20 to +70°C	≤ ± 100 ppb	≤ ± 50 ppb	
$C = -40 \text{ to } +85^{\circ}C$	≤ ± 150 ppb	≤ ± 100 ppb	

TABLE 2: Idd

Temperature	Vdd = 3.3V	Vdd = 5V
25°C	≤ 120 mA	≤ 80 mA
-20°C	≤ 170 mA	≤ 120 mA
start-up current at 25°C	≤ 350mA	≤ 300mA
duration	30s	10s

TABLE 3:

Frequency control adjustment response slope positive	Vdd = 3.3V	Vdd = 5V
Voltage control input impedance > 47kΩ	0 to 3.3V	0.5 to 5V
Resistor control R connect pin 1 to ground (Input impedance > -4,7kΩ)	0 to 10kΩ	0 to 10kΩ
No frequency control YA or YB	Pin 1 connect to GND	



STANDARD FREQUENCIES:

Frequency «MHz»			
10	20	40	50
54	100	108	120
Other frequencies from 10 MHz up to 120 MHz on request			

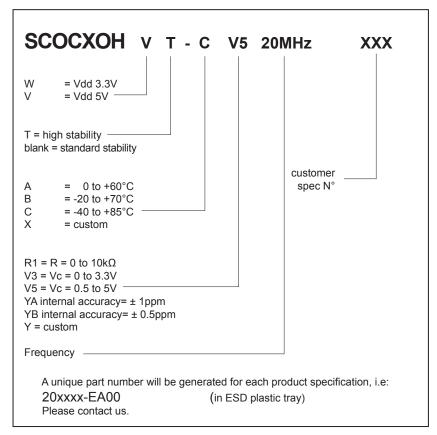
ENVIRONMENTAL CHARACTERISTICS:

Storage temp. range	-55 to +125°C
Vibration resistance	10 to 2000Hz / 20g
Shocks resistance	5000g / 0.3ms / ½ sine

TERMINATIONS AND PROCESSING:

Pin soldering	+235°C / 10s max +260°C / 5s max
Package SMD version option D1 or D2 see application note	Dil 14.4 pins GND to case height = 8mm

PRODUCT DESCRIPTION AND ORDERING INFORMATION:



All specifications subject to change without notice.



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