XO5019 Series

2x2 inch, 12.0 Volt, CMOS/Sinewave, OCXO





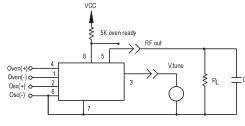
THIS PRODUCT IS NOT RECOMMENDED FOR NEW DESIGNS.

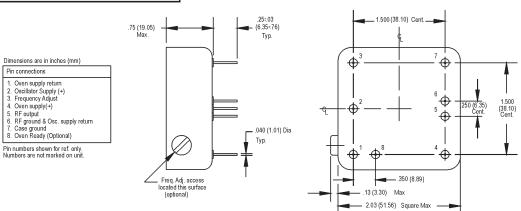
- · Low aging and phase noise
- External or voltage control

Model XO5019		Temperature Range (°C)	Temperature Stability	Aging First Year	Output	Supply Voltage
SC-Cut	10	-20 to +70	±1.0 x 10 ⁻⁸	±1.5 x 10 ⁻⁷	Sinewave	+15 Vdc oven (+12 Vdc osc)
AT-Cut	10	-20 to +70	±5.0 x 10 ⁻⁸	±2.0 x 10 ⁻⁷	Sinewave	+15 Vdc oven (+12 Vdc osc)
Options	5 to 15	See	Table	Consult Factory	TTL/CMOS	+10 Vdc to 24 Vdc

	PARAMETER	SC-cut	AT-cut	
	Frequency Range	10 MHz		
	Temperature Range	-20°C to +70°C		
	Temperature Stability	±1.0 x 10 ⁻⁸	±5.0 x 10 ⁻⁸	
	Supply Voltage	+15 Vdc oven (+12 Vdc osc)	+15 Vdc oven (+12 Vdc osc)	
	Aging Over 1 Year	±1.5 x 10 ⁻⁷	±2.0 x 10 ⁻⁷	
	Aging Per Day	±5.0 x 10 ⁻¹⁰		
Electrical Specifications	Turn On	600 mA max		
	Current	300 mA max @ 25°C steady state		
	Warm-up Time @ 25°C	To within ±0.10 ppm of Fo in 2.5 min, ±0.01 ppm in 5 min		
	Load Stability	±1.0 x 10 ⁻⁹		
	Aging Adjustment	External 10 k pot/voltage		
	Voltage Range	0.5 V to 10 V		
ij	Deviation	±3.0 x 10 ⁻⁷		
bed	Slope	Positive		
trical S	Phase Noise			
	1 Hz	-90 dBc/Hz	-75 dBc/Hz	
lect	10 Hz	-120 dBc/Hz	-110 dBc/Hz	
ш	100 Hz	-145 dBc/Hz	-140 dBc/Hz	
	1 kHz	-155 dBc/Hz	-155 dBc/Hz	
	10 kHz	-160 dBc/Hz	-160 dBc/Hz	
	Sinewave Version			
	Spurious	-75 dBc max		
	Harmonics	-20 dBc max		
	Load	50 Ω to 3 k Ω		
	Output Level	+7 dBm min into 50 Ω		
	HCMOS Version			
	Duty Cycle	40/60		
	Load	2 Gates		

Optional Temperature Ranges and Frequncy Stabilities						
Temperature Range (°C)	Stability Limits					
	SC-cut	AT-cut				
0 to +50	±2.0 x 10 ⁻⁹	±1.0 x 10 ⁻⁸				
0 to +70	±3.0 x 10 ⁻⁹	±3.0 x 10 ⁻⁸				
-10 to +70	±4.0 x 10 ⁻⁹	±3.0 x 10 ⁻⁸				
-30 to +70	±5.0 x 10 ⁻⁹	±5.0 x 10 ⁻⁸				
-40 to +70	±7.0 x 10 ⁻⁹	±1.0 x 10 ⁻⁷				
-55 to +70	±3.0 x 10 ⁻⁹	±5.0 x 10 ⁻⁷				





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