

CMC208-SERIES



- Ultra Low power: < 1 μ A
- Fixed 32.768 kHz
- No Supply Voltage external bypass capacitors required
- XTAL replacement in 2.0 mm x 1.2 mm SMD
- SMD package 2.0 x 1.2 mm

ELECTRICAL SPECIFICATIONS

PARAMETER	SYMBOL	CONDITION	VALUE			UNIT
			Min.	Typ.	Max.	
Frequency nominal	f_0			32.768		kHz
Supply Voltage	V_s	$T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$ $T_a = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$	1.2 1.5		3.63 3.63	V V
Core Supply Current	I_s	$V_s = 1.8\text{V}$, no load condition, $T_a = 25^{\circ}\text{C}$		0.9		μA
		$V_s = 3.63\text{V}$ max, $T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$ no load condition			1.3	μA
		$V_s = 3.63\text{V}$ max, $T_a = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ no load condition				1.4
Is does not include output stage current or load. To derive total operating current(no load) ,add $I_s + (0.065\mu\text{A})$ (peak to peak output Voltage swing)						
Output Stage Supply Current	$I_{s_{out}}$	$V_s = 1.5\text{V} \sim 3.63\text{V}$ max, $T_a = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$,no load condition		0.065	0.125	$\mu\text{A/Vpp}$
Operating Temperature	T_a	Commercial Industrial	-10 -40		+70 +85	$^{\circ}\text{C}$ $^{\circ}\text{C}$
Frequency Tolerance	$\Delta f/f_0$	After two reflows, at 25°C $V_s = 1.5\text{V} \sim 3.63\text{V}$			20	ppm
Frequency Stability	$\Delta f/f_0$	$T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$, $V_s = 1.5 \sim 3.63\text{V}$			75	ppm
		$T_a = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_s = 1.5 \sim 3.63\text{V}$			100	ppm
		$T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$, $V_s = 1.2 \sim 1.5\text{V}$			250	ppm
Measured peak to peak, inclusive of initial tolerance at 25°C , variations over operating temperature range, rated supply voltage and load						
Power supply Ramp		$T_a = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, 0 to 90% V_s			100	ms
Start-up Time	T_{START}	$T_a = -40^{\circ}\text{C} \leq T_a \leq +50^{\circ}\text{C}$, valid output		180	300	ms
		$T_a = 50^{\circ}\text{C} \leq T_a \leq +85^{\circ}\text{C}$, valid output			450	ms

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OUTPUT CHARACTERISTICS

	PARAMETER	SYMBOL	CONDITION	VALUE			UNIT
				Min	Typ.	Max	
LVCMOS	Output Levels	V_{OH}	$V_S = 1.5V \sim 3.63V, I_{OH} = -10\mu A, 15 pF$	0.9 V_S			V
		V_{OL}	$V_S = 1.5V \sim 3.63V, I_{OL} = 10\mu A, 15 pF$			0.1 V_S	V
	Duty Cycle	DC		48		52	%
	Rise/ Fall Time	T_r / T_f	10-90% $V_S, 15 pF$ Load, $V_S = 1.5V$ to 3.63V 10-90% $V_S, 5 pF$ Load, $V_S \geq 1.62V$		100	200 50	ns ns

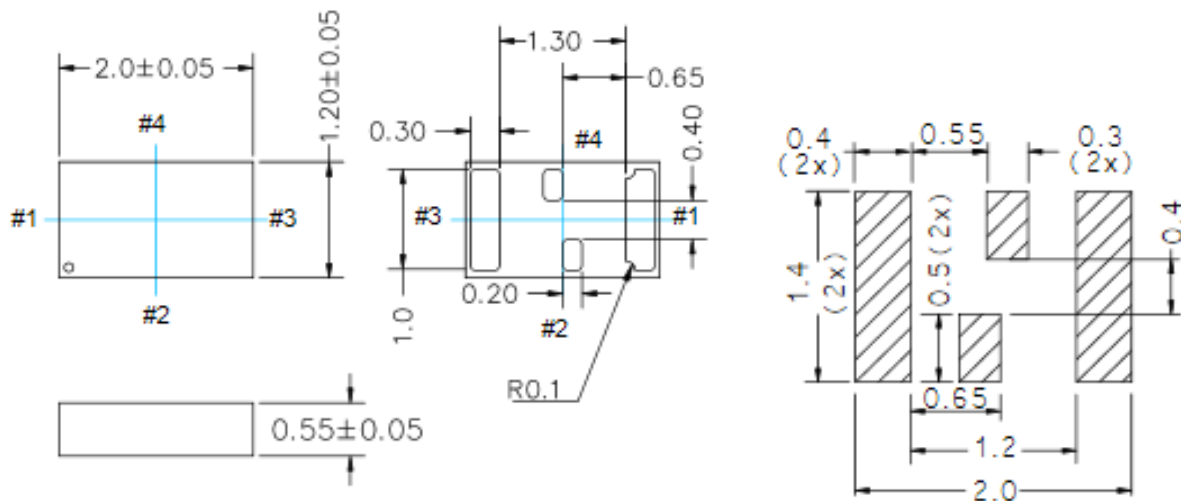
	PARAMETER	SYMBOL	CONDITION	VALUE			UNIT
				Min	Typ.	Max	
Nano Drive™ Programmable Reduced Swing Output	Output Levels	V_{OH}	DC bias programmable output voltage High range $V_S = 1.5V \sim 3.63V, I_{OH} = -0.2 \mu A, 10pF$ Load See Table 1		0.6 to 1.225		V
		V_{OL}	DC bias programmable output voltage Low range $V_S = 1.5V \sim 3.63V, I_{OH} = 0.2 \mu A, 10pF$ Load See Table 1		0.35 to 0.80		V
	Duty Cycle	DC		48		52	%
	Rise/ Fall Time	T_r / T_f	30-70% (V_{OL} / V_{OH}), 10 pF Load			200	ns
	AC- couple Programmable Output Swing	V_{sw}	$V_S = 1.5V \sim 3.63V, 10 pF$ Load, $I_{OH} / I_{OL} = \pm 0.2 \mu A$ This output is intended for a receiver that is AC- coupled, see table 1		0.20 to 0.80		V
	Programmable Output Voltage Swing Tolerance		$T_a = -40^\circ C$ to $+85^\circ C, V_S = 1.5 \sim 3.63V$	-0.055		0.055	V
	Period Jitter RMS	J p	Cycles = 10000, $T_a = 25^\circ C, V_S = 1.5 \sim 3.63V$		35		ns

Table1. NanoDrive™ Output levels

NanoDrive™	V_{OH} (V)	V_{OL} (V)	Swing (mV)	
D26	1.2	0.6	600±55	1.8V logic compatible
D14	1.1	0.4	700±55	1.8V logic compatible
D74	0.7	0.4	300±55	XTAL compatible
AA3	-	-	300±55	XTAL compatible

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MECHANICAL DIMENSIONS AND PIN FUNCTIONING



PIN	SYMBOL	FUNCTION
1	NC	No connect. This pin will not respond to any input signal. It can be left floating
2	GND	Electrical Ground
3	OUTPUT	Output Signal ¹
4	Vs	Supply Voltage ²

Note: 1. Oscillator output signal. When interfacing to an MCU's XTAL, the CLK Out is typically connected to the Receiving IC's XIN pin. Oscillator includes an internal driver. The output swing and operation is not dependent on capacitive loading.

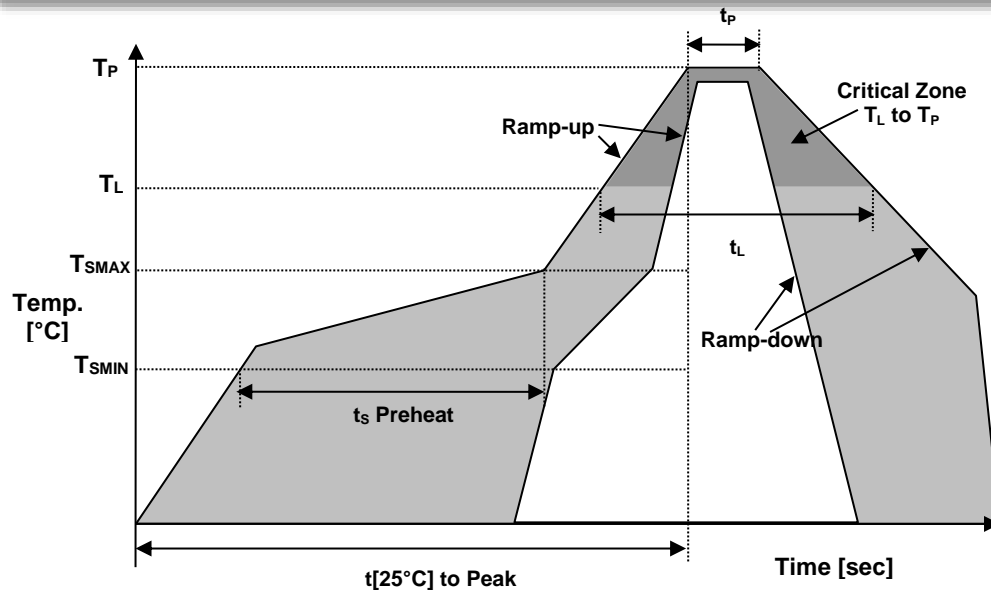
2. Connect to supply voltage $1.5V \leq V_s \leq 3.63V$ for operation over $-40^\circ C$ to $+85^\circ C$ temperature range. Under normal operation Conditions, V_s does not require external bypass/ decoupling capacitor(s). Internal power supply filtering will reject more than $\pm 150mV$ p-p with frequency components through 10MHz.

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ENVIRONMENTAL

Soldering	MIL-STD-883F, Method 2003
Moisture Sensitivity Level	MSL 1 at 260°C
Temperature Cycle	JESD22, Method A104
Vibration	MIL-STD-883F, Method 2007
Mechanical Shock	MIL-STD-883F, Method 2002
Storage Temperature	-65° +150°C

REFLOW PROFILE



Recommended Solder Reflow Profile		
Temperature Min Preheat	T_{SMIN}	150°C
Temperature Max Preheat	T_{SMAX}	200°C
Time (T_{SMIN} to T_{SMAX})	t_s	60-180 sec.
Temperature	T_L	217°C
Peak Temperature	T_P	260°C
Ramp-up rate	R_{UP}	3°C/sec max.
Ramp-down rate	R_{DOWN}	6°C/sec max.
Time within 5°C of Peak Temperature	t_p	10 sec
Time $t[25°C]$ to Peak Temperature	$t[25°C]$ to Peak	480 sec.
Time	t_L	60-150 sec.

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ORDERING INFORMATION

SERIES	SUPPLY VOLTAGE (V)	Frequency Stability	TEMP RANGE (°C)	Output Signal	-	OUTPUT FREQUENCY (kHz)
CMC208	12: Vs=1.2V~3.6V 15: Vs=1.5V~3.6V	A: ±75 ppm B: ±100ppm C: ±200ppm	U: -10~70 V: -40~85	1: LVCMOS NanoDrive™ Reduced Swing Output setting options A: AC-coupled D: DC- coupled 26:D26 14:D14 74:D74 3:AA3 See table 1	-	32.768

APPROVALS

Eng. approval, date: SP, 07 / 21 / 2016

Created by, date: SP, 07 / 21 / 2016

Revision: A