

Main Feature

- RoHS compliant
- Seam Sealed Ceramic SMD type
- Cross Ref. No. ASVV and more
- Supply Voltage in 3.3V & 5.0V



7.0*5.0*1.8mm

Application

- Communication, Test equipment
- Computers, Modems, Microprocessors

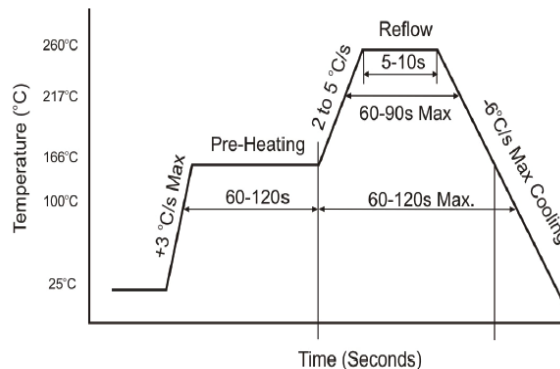
Standard Specification

Frequency Range: 1.0000MHz ~ 200.000MHz
 Supply Voltage: 3.3V (Typ.) or Specify 1.8V, 5.0V
 Frequency Stability: $\pm 10 \sim \pm 100$ ppm
 Operating Temperature: $-0^{\circ}\text{C} \sim +70^{\circ}\text{C}$ OR $-40^{\circ}\text{C} \sim +85^{\circ}\text{C}$
 Symmetry: 40/60 or 45/55
 Storage Temperature: $-55^{\circ}\text{C} \sim +125^{\circ}\text{C}$
 Aging (at 25°C): ± 5 ppm Max.
 Output Load: 1-10TTL//15pF or HCMOS 15pF; 30pF; 50pF
 Start up Time: 10 ms Max.
 Rise/Fall Time: 6 ns Max.
 Output Voltage: 10% Max. and 90% Min.
 Linearity: $\pm 10\%$
 Enable Input: Input High ($> 2.2\text{V}$) or Floating
 Enable/Disable Time: Input Low ($< 0.8\text{V}$)
 Package: 1000pcs/Reel

Enable/Disable Funtion

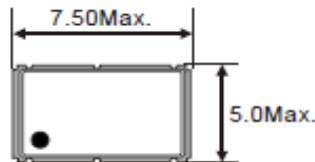
Input (Pin2)	Output (pin 4)
Open	Enabled
"1" Level	Enabled
"0" Level	High Impedance

Reflow Profile Condition(260 ° C Max.)

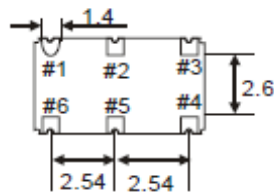


Dimension (Unit: mm)

Top View



Bottom View

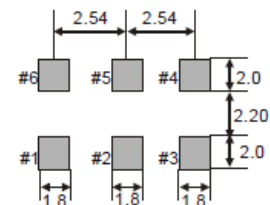


Side View



Solder Pattern

PIN	CONNECTION
#1	Control Voltage
#2	Enable/Disable
#3	Ground
#4	Output
#5	Not connected
#6	Supply



TGS part No. Guide

TGS	VM75	3	2	C	B	H	01	T	LF	XX	-27M0000	
		1	2	3	4	5	6	7	8	9	10	11

- 1) VM75: TGS Part Family No. and SMD VCX0, 7.0*5.0mm
- 2) Supply Voltage; 5-5.0V; 3-3.3V; 1- 1.8V
- 3) Enable/Disable Funtion: 2-Yes via pin 2; 0-None
- 4) Frequency Stability: A- ± 25 ppm; B: ± 50 ppm; C: ± 100 ppm; J- ± 20 ppm
- 5) Operating Temp: D- $0 \sim 70^{\circ}\text{C}$; E: $-10 \sim +60^{\circ}\text{C}$; F: $-20 \sim +70^{\circ}\text{C}$; G: $-40 \sim +85^{\circ}\text{C}$
- 6) Symmetry: H: 40/60; I: 45/55
- 7) Output Load: 01: 1-10TTL//15pF; 02:HCMOS 15pF; 03: HCMOS 30pF; 04: HCMOS 50pF
- 8) T: Tape/Reel 9) LF: RoHS compliant 10) XX: 2 letters as Internal Control Code
- 11) Frequency Range in MHz or specify