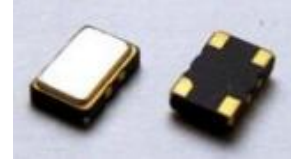


Main Feature

- Low current consumption
- Cross Ref. No. ASF1/ASFL1/ASFL2
- Seam sealed package assures high reliability
- Suitable for RoHS compliant reflow process

Application

- CCD clock for VTR Camera
- Thin equipment



5.0\*3.2\*1.3mm

Standard Specification

Frequency Range: 1.00MHz ~ 156.0MHz  
 Supply Voltage: 3.3V, 2.5V, 5.0V  
 Overall Frequency Stability:  $\pm 20\text{ppm} \sim \pm 100\text{ppm}$   
 Symmetry: 40/60 or 45/55  
 Operating Temperature:  $-20 \sim +70 \text{ C}; -40 \sim +85 \text{ C}$   
 Storage Temperature:  $-55 \text{ }^\circ\text{C} \sim +125^\circ\text{C}$   
 Aging (at 25°C):  $\pm 5\text{ppm Max.}$   
 Output Load: CMOS 15pF  
 Start up Time: 10 ms Max.  
 Rise/Fall Time: 10 ns Max.  
 Output Voltage (VOH): 0.9V<sub>DD</sub> V  
 Output Voltage (VOL): 0.10V<sub>DD</sub> V  
 Package: 1000pcs/Reel  
 Phase Jitter RMS: 1pS Max.

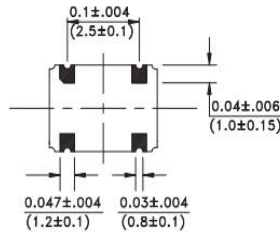
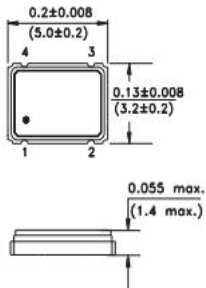
Supply Current

Frequency Range in MHz	Current Max. (mA)
1.000~29.999 (3.3V)	15.0mA Max
30.000~79.999 (3.3V)	45.0mA Max
80.000~156.000 (3.3V)	85.0mA Max
1.000~29.999 (2.5V)	15.0mA Max
30.000~79.999 (2.5V)	45.0mA Max
80.000~156.000 (2.5V)	85.0mA Max
1.000~29.999 (5.0V)	15.0mA Max
30.000~79.999 (5.0V)	45.0mA Max
30.000~60.000 (5.0V)	85.0mA Max

Dimension (Unit: mm)

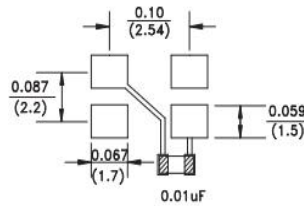
Top View

Bottom View



Pin	Function
1	Tri-State (STBY)
2	GND/Case
3	Output
4	Vdd

Solder Pattern



TGS part No. Guide

TGS	VTM53	3	1	B	D	H	01	T	LF	XX	-27M0000	
		1	2	3	4	5	6	7	8	9	10	11

- VTM53: TGS Part Family No.
- Supply Voltage; 5-5.0V; 3-3.3V; 2-2.5;
- Tri-state: 1-Yes; 0-None
- Frequency Stability: A:  $\pm 25\text{ppm}$ ; B:  $\pm 50\text{ppm}$ ; C:  $\pm 100\text{ppm}$ ; D:  $\pm 30\text{ppm}$  J  $\pm 20\text{ppm}$
- Operating Temp: D:  $0 \sim 70 \text{ C}$ ; E:  $-10 \sim +60 \text{ C}$ ; F:  $-20 \sim +70 \text{ C}$ ; G:  $-40 \sim +85 \text{ C}$
- Symmetry: H: 40/60; I: 45/55
- Output Load: 01: CMOS 15pF;
- T: Tube
- LF: RoHS compliant
- XX: 2 letters as Internal Control Code
- Frequency Range in MHz or specify